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JDobbs  
P. Bedrosian 7

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application

Applicant(s): P. Bedrosian  
Case: 7  
Serial No.: 09/332,726  
Filing Date: June 14, 1999  
Group: 2663  
Examiner: Jasper C. Kwoh

I hereby certify that this paper is being deposited on this date with the U.S. Postal Service as first class mail addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Signature: Lisa L. Tulpis Date: June 30, 2003

Title: Method and Apparatus for Synchronization of  
High-Bit-Rate Digital Subscriber Line Signals

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Technology Center 2600

TRANSMITTAL OF FORMAL DRAWINGS

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Attention: Official Draftsperson

Sir:

Applicant submits herewith two (2) sheets of formal drawings in the above-referenced patent application.

Respectfully submitted,

Date: June 30, 2003

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